

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Jeffrey S. Kuskin, et al.  
Serial No.: Unknown  
Filing Date: December 2, 2003  
Group Art Unit: Unknown  
Examiner: Unknown  
Title: SYSTEM AND METHOD FOR REDUCING  
MEMORY LATENCY DURING READ  
REQUESTS

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

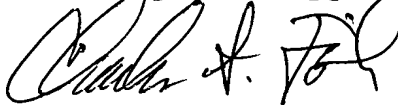
Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97, and 1.98, that the documents listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), Applicant makes no representation that these documents qualify as prior art or that these documents are material to patentability of the present application or that a search has been made.

REMARKS

The present Information Disclosure Statement is being filed in a continuation application of U.S. Serial No. 09/909,701 filed July 20, 2001. Each document listed on the attached PTO Form 1449 was cited by or submitted to the U.S. Patent and Trademark Office in the prior application as properly identified above. Therefore, pursuant to 37 C.F.R. § 1.98(d), a copy of each listed document need not be provided with the present Information Disclosure Statement.

Respectfully submitted,

BAKER BOTTS L.L.P.  
Attorneys for Applicant



Charles S. Fish  
Reg. No. 35,870

Date: December 2, 2003

Correspondence Address

Baker Botts L.L.P.  
2001 Ross Avenue  
Suite 600  
Dallas, Texas 75201  
214/953-6507

  X   Customer Number 05073

PTO-1449		Application No. Unknown		Applicant(s) Jeffrey S. Kuskin, et al.	
Information Disclosure Citation In an Application		Docket Number 062986.0297		Group Art Unit Unknown	
				Filing Date November 13, 2003	

## U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A	6,338,122	01-08-2002	Baumgartner, et al.			
	B	6,073,211	6-6-2000	Cheng, et al.			
	C	5,537,569	7-16-1996	Masubuchi, Yoshio			
	D	6,192,452	2-20-2001	Bannister, et al.			
	E	5,606,686	2-25-1997	Tarui, et al.			
	F	6,279,084	8-21-2001	VanDoren, et al			
	G						
	H						
	I						
	J						
	K						
	L						
	M						
	N						
	O						
	P						
	Q						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	R							
	S							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	T	Culler, et al. Parallel Computer Architecture", 1999 Morgan Kaufmann Publishers, pp.553-589.	
	U		
		DATE CONSIDERED	

## EXAMINER

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE